--------------- Build Started: 08/19/2019 23:40:39 Project: IDAC8\_Example01, Configuration: ARM GCC 5.4-2016-q2-update Debug ---------------

The code generation step is up to date.

The compile step is up to date, no work needs to be done.

arm-none-eabi-ar.exe -rs .\ARM\_GCC\_541\Debug\IDAC8\_Example01.a .\ARM\_GCC\_541\Debug\IDAC8\_1.o .\ARM\_GCC\_541\Debug\IDAC8\_1\_PM.o .\ARM\_GCC\_541\Debug\Pin\_1.o .\ARM\_GCC\_541\Debug\CyDmac.o .\ARM\_GCC\_541\Debug\CyFlash.o .\ARM\_GCC\_541\Debug\CyLib.o .\ARM\_GCC\_541\Debug\cyPm.o .\ARM\_GCC\_541\Debug\CySpc.o .\ARM\_GCC\_541\Debug\cyutils.o .\ARM\_GCC\_541\Debug\cy\_em\_eeprom.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1\_PM.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1\_INT.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1\_AMux.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1\_Ext\_CP\_Clk.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1\_IRQ.o .\ARM\_GCC\_541\Debug\ADC\_DelSig\_1\_theACLK.o .\ARM\_GCC\_541\Debug\UART\_1.o .\ARM\_GCC\_541\Debug\UART\_1\_PM.o .\ARM\_GCC\_541\Debug\UART\_1\_INT.o .\ARM\_GCC\_541\Debug\UART\_1\_BOOT.o .\ARM\_GCC\_541\Debug\UART\_1\_IntClock.o .\ARM\_GCC\_541\Debug\CyBootAsmGnu.o

arm-none-eabi-ar.exe: creating .\ARM\_GCC\_541\Debug\IDAC8\_Example01.a

arm-none-eabi-gcc.exe -Wl,--start-group -o "C:\Users\sebas\Documents\PSoC Creator\PSoC RTD Tests-20190621T134154Z-001\PSoC RTD Tests\IDAC8\_Example01.cydsn\ARM\_GCC\_541\Debug\IDAC8\_Example01.elf" .\ARM\_GCC\_541\Debug\main.o .\ARM\_GCC\_541\Debug\cyfitter\_cfg.o .\ARM\_GCC\_541\Debug\cymetadata.o .\ARM\_GCC\_541\Debug\Cm3Start.o .\ARM\_GCC\_541\Debug\IDAC8\_Example01.a "C:\Program Files (x86)\Cypress\PSoC Creator\4.2\PSoC Creator\psoc\content\cycomponentlibrary\CyComponentLibrary.cylib\CortexM3\ARM\_GCC\_541\Debug\CyComponentLibrary.a" -mcpu=cortex-m3 -mthumb -L Generated\_Source\PSoC5 -Wl,-Map,.\ARM\_GCC\_541\Debug/IDAC8\_Example01.map -T Generated\_Source\PSoC5\cm3gcc.ld -specs=nano.specs -u \_printf\_float -Wl,--gc-sections -g -ffunction-sections -O0 -ffat-lto-objects -Wl,--end-group

cyelftool.exe -C "C:\Users\sebas\Documents\PSoC Creator\PSoC RTD Tests-20190621T134154Z-001\PSoC RTD Tests\IDAC8\_Example01.cydsn\ARM\_GCC\_541\Debug\IDAC8\_Example01.elf" --flash\_row\_size 256 --flash\_size 262144 --flash\_offset 0x00000000

No ELF section .cychecksum found, creating one

Application checksum calculated and stored in ELF section .cychecksum

Checksum calculated and stored in ELF section .cymeta

cyelftool.exe -S "C:\Users\sebas\Documents\PSoC Creator\PSoC RTD Tests-20190621T134154Z-001\PSoC RTD Tests\IDAC8\_Example01.cydsn\ARM\_GCC\_541\Debug\IDAC8\_Example01.elf"

Flash used: 17828 of 262144 bytes (6.8%).

SRAM used: 2637 of 65536 bytes (4.0%). Stack: 2048 bytes. Heap: 128 bytes.

--------------- Build Succeeded: 08/19/2019 23:40:40 ---------------